

REMARKS

Present Status of the Application

It is noted that the previously submitted amendment necessitated the new grounds of rejection, and accordingly this action is made final. Currently, claims 12-17 and 21-24 remain pending in the current application.

The Office has rejected all of the pending claims 12-17 and 21-24 under 35 U.S.C. Section 102(a) as being assertedly anticipated by Asano et al. (U.S. Publication No. 2002/0190924 A1; "Asano" hereinafter).

After carefully considering the rejection set forth in this Office action and the cited reference, Applicant respectfully traverses the prior art rejection upon the reasons indicated hereinafter. Reconsideration and withdrawal of the rejection raised in the outstanding action are respectfully requested.

Discussion of Office Action Rejections under 35 U.S.C 102(a)

The Office has rejected claims 12-17 and 21-22 under 35 U.S.C. Section 102(a), as being anticipated by Asano.

In response thereto, Applicant hereby otherwise disagrees and traverses the above rejection as follows.

The independent claims 12 and 21 at issue are narrated as follows,

"12. An organic light-emitting display, comprising:

a pixel array having a plurality of data lines, a plurality of scan lines and a plurality of first and second pixels, wherein each of the first and second pixels is electrically connected to one of the scan lines and one of the data lines correspondingly;

a **first external power line**, dividing into a plurality of **first internal power lines**, wherein each first internal power line is directly connected to at least two of the first pixels;

a **second external power line**, dividing into a plurality of **second internal power lines**, wherein each second internal power line is directly connected to at least two of the second pixels, and the first internal power lines and the second internal power lines are separated; and

a **power source electrically connected to the first and second external power lines**, wherein the first external power line and the second external power line provide a same power signal to the first pixels and the second pixels.”(Emphasis added)

“21. An organic light-emitting display, comprising:

a pixel array having a plurality of data lines, a plurality of scan lines and a plurality of first and second pixels arranged in a matrix of columns and rows, wherein each of the first and second pixels is electrically connected to one of the scan lines and one of the data lines correspondingly;

a **first external power line**, dividing into a plurality of **first internal power lines**, wherein each first internal power line is directly connected to the first pixels in the same column or in the same row;

a **second external power line**, dividing into a plurality of **second internal power lines**, wherein each second internal power line is directly connected to the second pixels in the same column or in the same row, wherein the first internal power lines and the second internal power lines are separated; and

a **power source electrically connected to the first and second external power lines**, wherein the first external power line and the second external power line provide a same power signal to the first pixels and the second pixels.” (**Emphasis added**)

In rejecting claims 12 and 21, the Office has respectively construed the common power line 14 and several vertical lines connected to the common power line 14 taught by Asano as the first external power line and the first internal power lines recited in claims 12 and 21 at issue. Besides, the common ground line 15 and several vertical lines connected to the common ground line 15 disclosed by Asano have been respectively considered by the Office as Applicant’s second external power line and second internal power lines. According to the action, the Office has held that the common power line 14 and the common ground line 15 taught by Asano are separated. In addition, V_o of Asano is assertedly corresponding to the power source of the present invention, and it is asserted by the Office that the first pixel and the second pixel are both supplied with the same power V_o according to FIG. 1 of Asano. Applicant respectfully dissents from the assertion made in the Office action for at least the following reasons.

With reference to FIG. 1 of Asano, Vo, the asserted power source, is merely connected to the common power line 14 but not connected to the common ground line 15. Specifically, the common ground line 15 in FIG. 1 of Asano is grounded rather than connected to Vo. That is to say, at least the feature **“a power source electrically connected to the first and second external power lines**, wherein the first external power line and the second external power line provide a same power signal to the first pixels and the second pixels” set forth in claims 12 and 21 at issue is not disclosed by the Asano reference.

By virtue of the foregoing, Applicant respectfully submits to the Office that the single reference Asano fails to render claims 12 and 21 of the invention anticipated for at least the reason not each and every element as set forth in the claims 12 and 21 is found, either expressly or inherently, in the single prior art reference. Claims 12 and 21 are thus patentable over Asano.

Since claims 13-17 and 22-24 are dependent claims which further define the invention recited in claims 12 and 21, respectively, Applicant respectfully asserts that these claims are also in condition for allowance. Hence, reconsideration and withdrawal of this 102 rejection are respectively requested.

Customer No.: 31561
Docket No.: 10217-US-PA
Application No.: 10/707,608

CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date :

June 4, 2008

Respectfully submitted,

Belinda Lee

Belinda Lee

Registration No.: 46,863

Jianq Chyun Intellectual Property Office
7th Floor-1, No. 100
Roosevelt Road, Section 2
Taipei, 100
Taiwan
Tel: 011-886-2-2369-2800
Fax: 011-886-2-2369-7233
Email: belinda@jcipgroup.com.tw
Usa@jcipgroup.com.tw